

## CLAIMS

What is claimed is:

1. A computer-readable medium having stored thereon a  
2 data structure comprising:

3 a first field containing data representing an  
4 assigned target; and

5 a second field containing data representing a logic  
6 value to be assigned to said assigned target.

1 2. The computer-readable medium of **claim 1**, wherein  
3 said first field and said second field are incorporated  
4 within a comment line of a design target entity source  
code file.

1 3. The computer-readable medium of **claim 2**, further  
2 comprising a pre-pended comment identifier field for  
3 identifying said data structure as a hardware description  
4 language comment.

1 4. The computer-readable medium of **claim 1**, wherein  
2 said assigned target is a simulation event, and wherein  
3 said first field further contains data declaring said  
4 simulation event.

1 5. The computer-readable medium of **claim 4**, wherein  
2 said first field further comprises an event type sub-  
3 field containing data for declaring a type of simulation  
4 event.

6. The computer-readable medium of **claim 1**, wherein said assigned target is an intermediate signal, and wherein said first field further contains data declaring said intermediate signal.

7. The computer-readable medium of **claim 1**, wherein said assigned logic value is a logical connectivity expression for combining a plurality of signals.

8. The computer-readable medium of **claim 7**, wherein said assigned target is an intermediate signal, and wherein at least one of said plurality of signals originates as said intermediate signal.

9. The computer-readable medium of **claim 1**, wherein said assigned logic value is a logic function for combining at least two signals.

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